



INFORMATION DISCLOSURE CITATION PTO-1449	Patent & Trademark Office. Docket No. 010704A	Serial No. 10/709,287
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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
<u>C7L</u>	AA	5,936,455	Kobayashi et al.	08/10/1999	327	437
_____	AB					
_____	AC					
_____	AD					
_____	AE					

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	Document No.	Date	Country	Translation (Yes or No)
<u>C7L</u>	AF 0 901 230	03/10/1999	Europe	Yes
<u>C7L</u>	AG 0 851 323	07/01/1998	Europe	Yes
_____	AH			
_____	AI			
_____	AJ			

OTHER DOCUMENTS

<u>C7L</u>	AK	Kim et al., "A Low-Power Half-Swing Clocking Scheme for Flip-Flop with Complementary Gate and Source Drive", IEICE TRANS. ELECTRON., Vol. E82-C, No. 9, September 1999, pages 1777-1779.
<u>C7L</u>	AL	Copy of European Patent Office Communication with European Search Report for corresponding European Patent Application No. 01410063 dated February 4, 2005.
Examiner	Cassandra Coy	
	Date Considered	April 13, 2005